

Attorney Docket No.01447/LH

**IN THE UNITED STATES PATENT
AND TRADEMARK OFFICE**

Applicant(s): M. UCHINO et al

Serial No. : Based on
PCT/JP00/09139

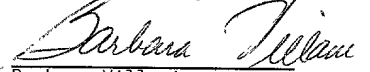
Filed : Herewith

For : WANDER GENERATOR,
AND DIGITAL LINE
TESTER AND PHASE
NOISE TRANSFER...

Art Unit :
Examiner :

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Barbara Villani

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PRELIMINARY AMENDMENT

Hon. Commissioner of Patents
and Trademarks

S I R :

IN THE SPECIFICATION:

Page 1: Please insert the following as the first sentence
(a copy of marked-up page 1 is attached):

--This application is a U.S. National Phase Application
under 35 USC 371 of International Application PCT/JP00/09139 (not
published in English) filed December 22, 2000.--

IN THE CLAIMS:

Please substitute amended claims 6 and 15 as follows:

6. (amended) A wander generator according to claim 3,
characterized by further comprising initial setting means for
initially setting values equivalent to stored values stored in
said respective storage elements in a steady state in which the
clock signal having the wander of the desired characteristic is

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being output to said respective storage elements included in the filter unit at least in an initial phase of operation of said apparatus through a path different from a signal input path in the steady state.

15. **(amended)** A wander generator according to claim 10, characterized in that said noise generating means has a plurality (m) of sets of pseudo random signal generating means for generating pseudo random codes of M sequence at initial phases different from one another, and is configured to collect outputs at predetermined stages of said respective pseudo random signal generating means to output an m-bit parallel white noise signal.

Please add the following new claims:

--22. **(new)** A wander generator according to claim 5, characterized by further comprising initial setting means for initially setting values equivalent to stored values stored in said respective storage elements in a steady state in which the clock signal having the wander of the desired characteristic is being output to said respective storage elements included in the filter unit at least in an initial phase of operation of said apparatus through a path different from a signal input path in the steady state.

23. **(new)** A wander generator according to claim 14, characterized in that said noise generating means has a plurality (m) of sets of pseudo random signal generating means for generating pseudo random codes of M sequence at initial

phases different from one another, and is configured to collect outputs at predetermined stages of said respective pseudo random signal generating means to output an m-bit parallel white noise signal.--

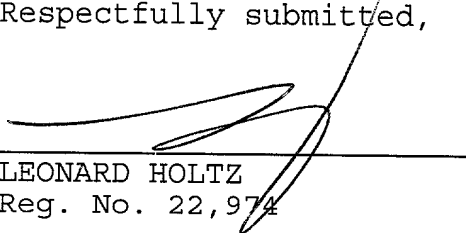
Approved for Release by NSA on 09-08-2013 pursuant to E.O. 13526

R E M A R K S

In accordance with 37 CFR 1.121(c), a clean copy of amended claims 6 and 15 is set forth in the present Amendment, and a marked-up version of the amended claims 6 and 15 is attached hereto.

The amendment is being made to eliminate the multiple dependencies of the claims.

Respectfully submitted,



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VERSION WITH MARKINGS TO SHOW CHANGES MADE

Claims 6 and 15 have been amended as follows:

6. **(amended)** A wander generator according to claim 3 [or 5], characterized by further comprising initial setting means for initially setting values equivalent to stored values stored in said respective storage elements in a steady state in which the clock signal having the wander of the desired characteristic is being output to said respective storage elements included in the filter unit at least in an initial phase of operation of said apparatus through a path different from a signal input path in the steady state.

15. **(amended)** A wander generator according to claim 10 [or 14], characterized in that said noise generating means has a plurality (m) of sets of pseudo random signal generating means for generating pseudo random codes of M sequence at initial phases different from one another, and is configured to collect outputs at predetermined stages of said respective pseudo random signal generating means to output an m-bit parallel white noise signal.

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DESCRIPTION

5 WANDER GENERATOR, AND DIGITAL LINE TESTER
AND PHASE NOISE TRANSFER CHARACTERISTIC
ANALYZER USING SAME

Technical Field

10 The present invention relates to a wander
generator, and a digital line tester and a phase noise
transfer characteristic analyzer using the same, and
more particularly, to a wander generator which
generates a clock signal having wander, and a digital
line tester and a phase noise transfer characteristic
analyzer using the same.

15 Among them, the phase noise transfer
characteristic analyzer is particularly related to a
phase noise transfer characteristic analyzer for
analyzing a transfer characteristic for a signal having
phase noise associated with a device which transmits a
20 clock signal or a digital signal, wherein the phase
noise transfer characteristic analyzer employs a
technique for properly evaluating a phase noise
transfer characteristic of a device under analysis in a
short measuring time.

25 Background Art

As is well known, a digital signal transmitted
over a digital line is affected by noise or the like on
a transmission path to experience fluctuating phase.

In the fluctuations of the phase, components in

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under 35 USC 371 of International Application PCT/JP00/09139 (not
published in English) filed December 22, 2000.

TRANSLATION

I, Yasuyuki Sasaki, residing at 1-3-1-204, Higashimonzen, Kawasaki-ku, Kawasaki-shi, Kanagawa-ken, Japan, state:

that I know well both the Japanese and English languages;

that I translated, from Japanese into English, the description, claims, abstract and drawings of International Application No. PCT/JP00/09139, filed December 22, 2000;

that the sheet next following this sheet is a copy of the Request of the said application as published as International Publication and is attached hereto in lieu of an English translation of the Request in the said application; and

that the attached English translation is a true and accurate translation to the best of my knowledge and belief.

Dated: July 23, 2001



Yasuyuki Sasaki

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